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ABSTRACT OF THE DISCLOSURE

A multiple protocol receiver includes a demodulator section. The
5 demodulator section includes a plurality of demodulators, each demodulator
having a tri-state output terminal for generating demodulated digital data. The
tri-state output terminals are coupled to a signal bus. The signal bus, in turn, is
coupled to a signal processor, such as a transport processor, for processing the
demodulated digital data.